

AMENDMENTS TO THE SPECIFICATION

Replace the paragraph at page 58, lines 21-30, as follows:

Referring to **Figs. 3a** and **3l** and to Table 1, X86 threads (*e.g.*, **302**, **304**) managed by X86 operating system **306**, carry the normal X86 context, including the X86 registers, as represented in the low-order halves of r32-r55, the EFLAGS bits that affect execution of X86 instructions, the current segment registers, etc. (The terms “process” and “thread” are used herein in their ordinary and customary, though formal, senses, as actually used in the programming language systems, operating systems, and processor architecture arts. Generally, a “process” is a unit of processor scheduling and protection, each with an associated data structure (or set of data structures) that, in most implementations, holds machine register values and other context associated with the process. The process data structures, and thus the processes of a computer, are usually under the management of an operating system, usually the operating system’s scheduler. Generally, a “thread” is a flow of control within a process. Each thread has an associated data structure (or set of data structures) that, in most implementations, hold machine register values (usually different than the registers associated with a process) and other context associated with the thread. The thread data structures, and thus the threads of a process, are usually managed either by an operating system or other run time system, to permit the thread to be scheduled independently of and concurrently with other threads of the same process.) In addition, if an X86 thread **302**, **304** calls native Tapestry libraries **308**, X86 thread **302**, **304** may embody a good deal of extended context, the portion of the Tapestry processor context beyond the content of the X86 architecture. A thread’s extended context may include the various Tapestry processor registers, general registers r1-r31 and r56-r63, and the high-order halves of r32-r55 (see Table 1), the current value of ISA bit **194** (and in the embodiment of section IV, *infra*, the current value of XP / calling convention bit **196** and semantic context field **206**).